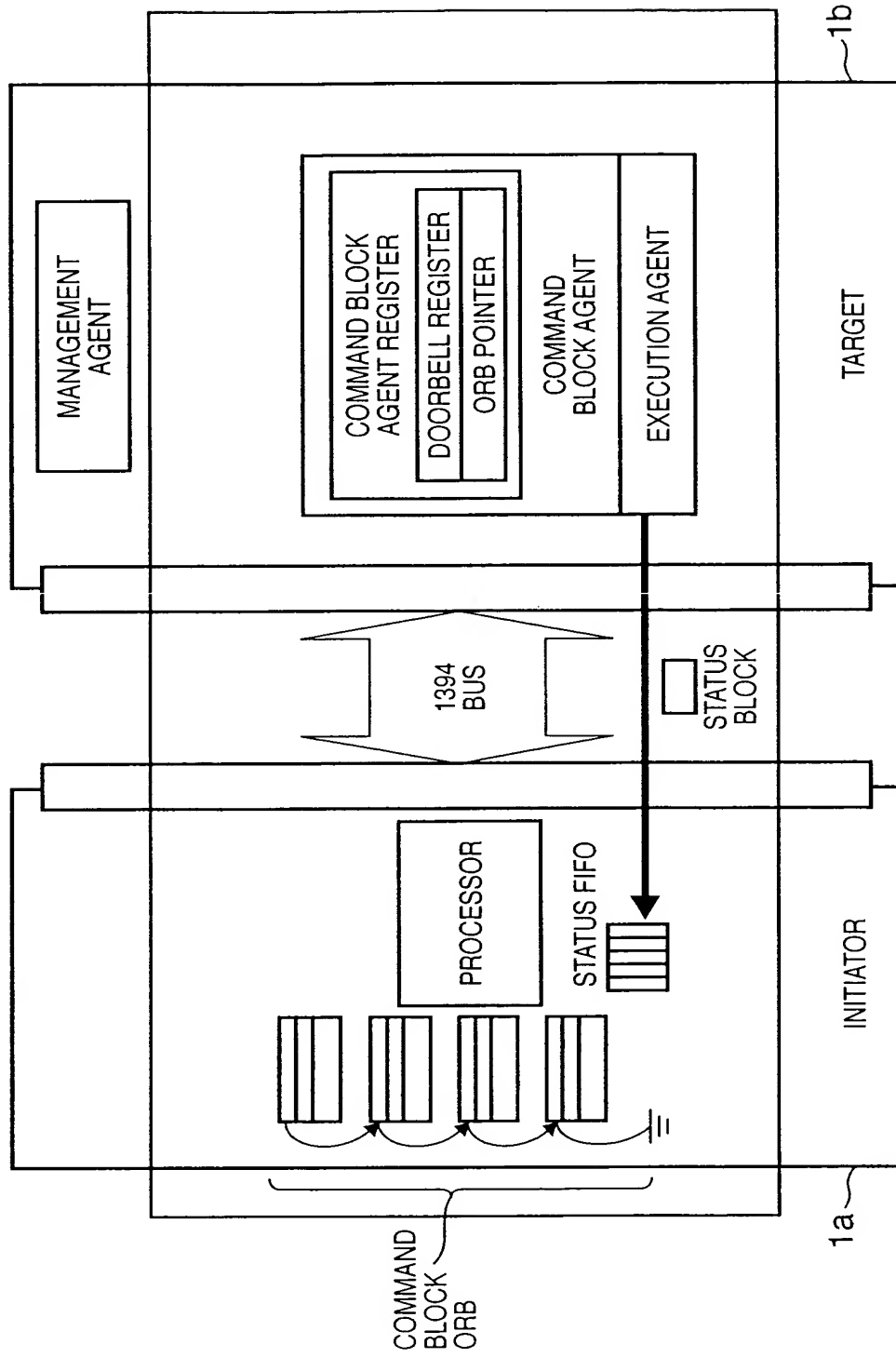
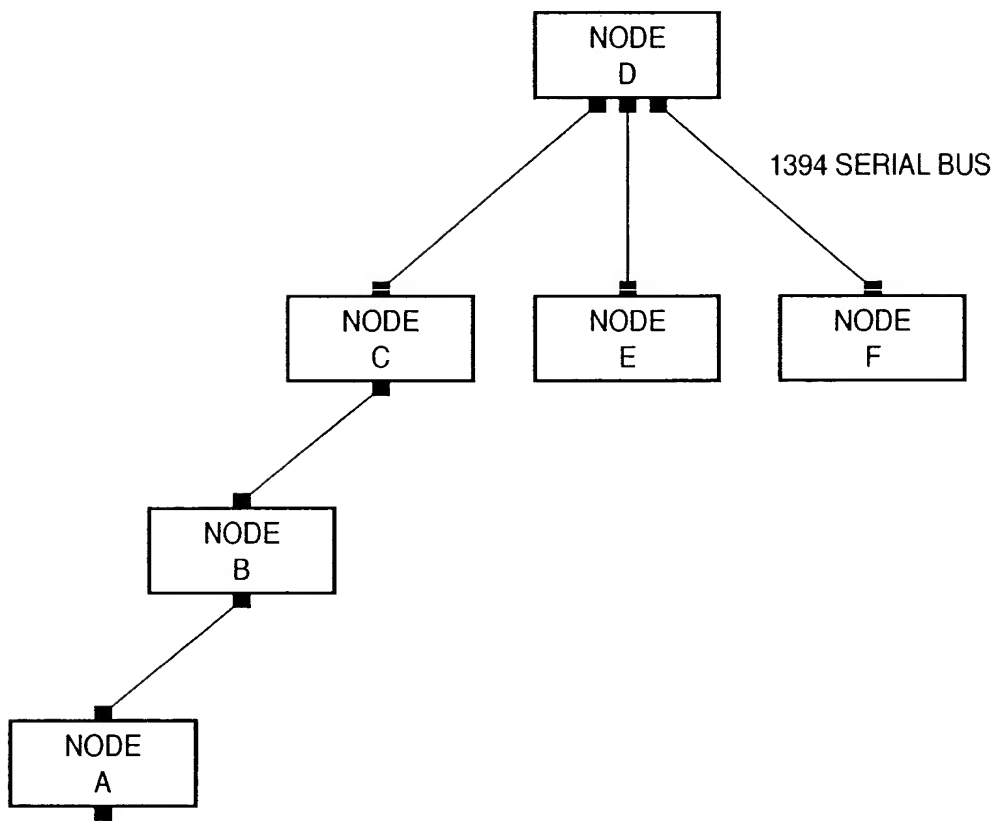
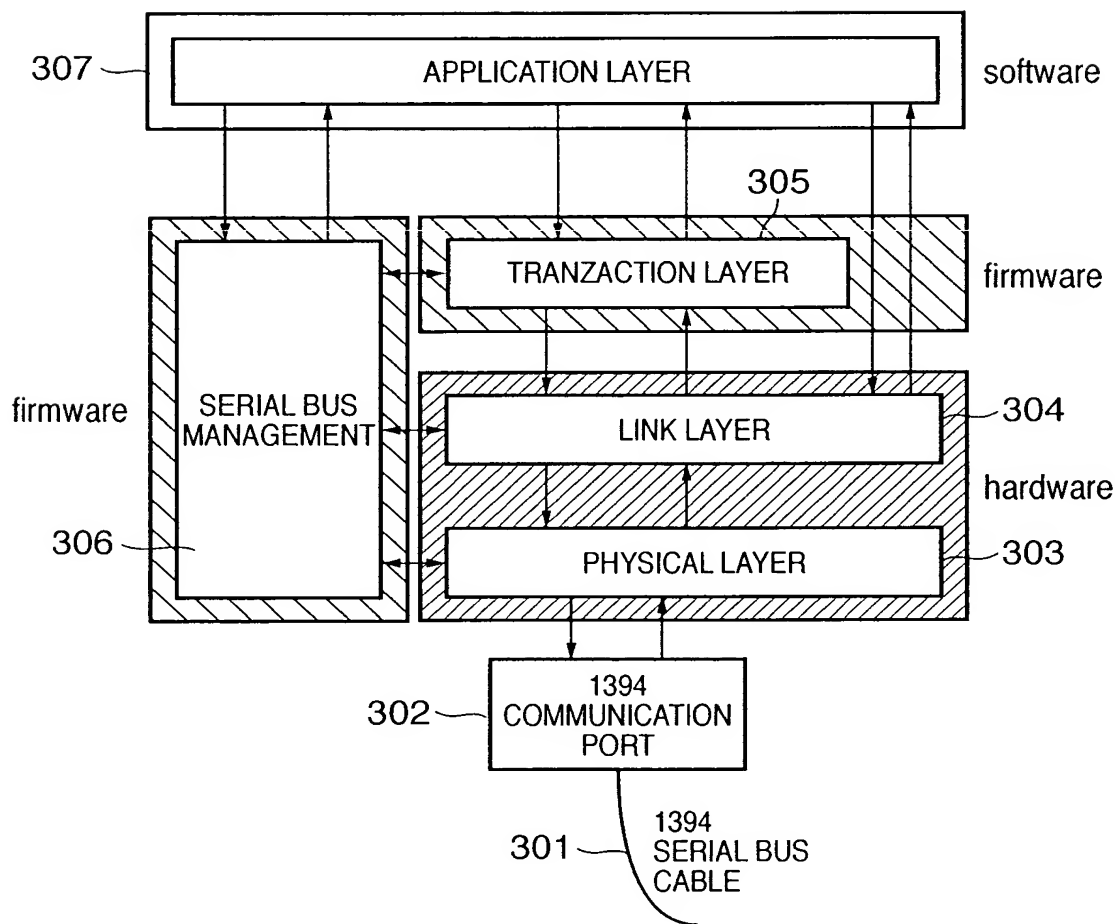
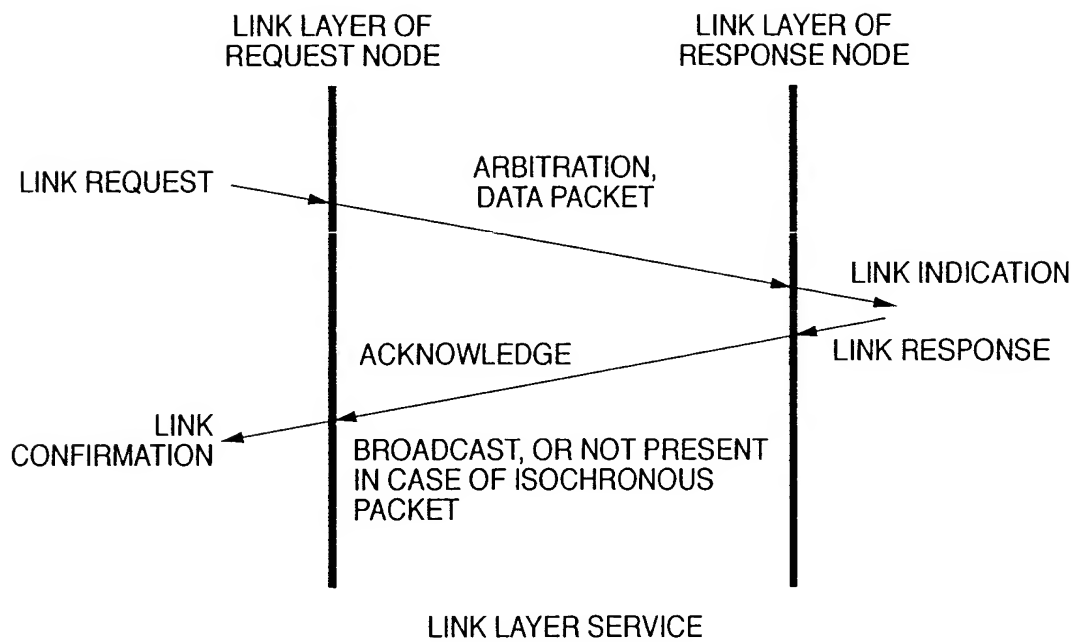


FIG. 1



**FIG. 2**

**FIG. 3**

**FIG. 4**

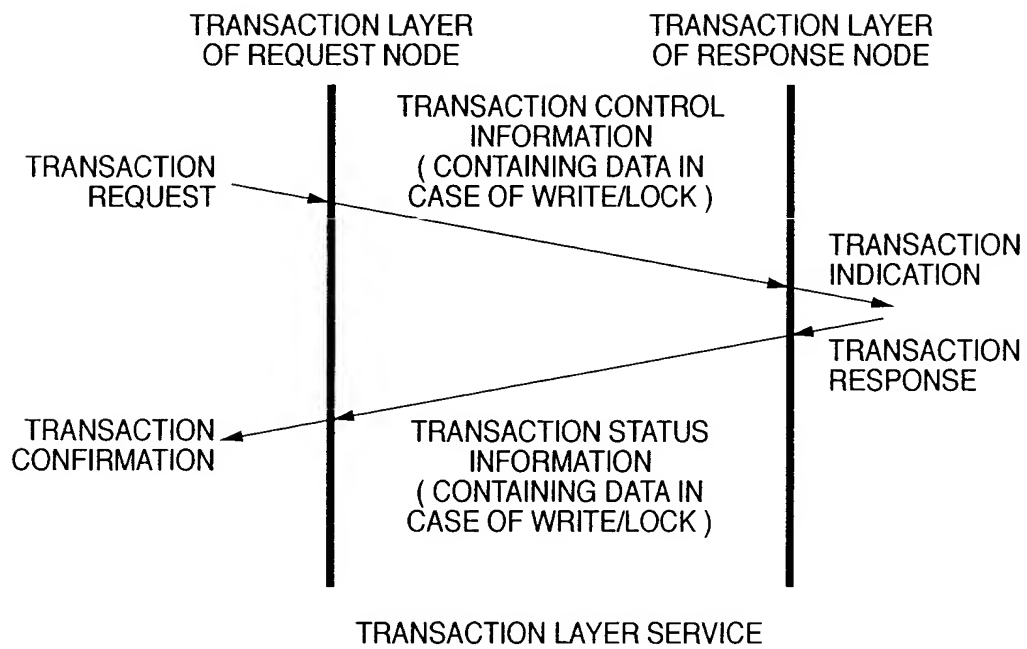
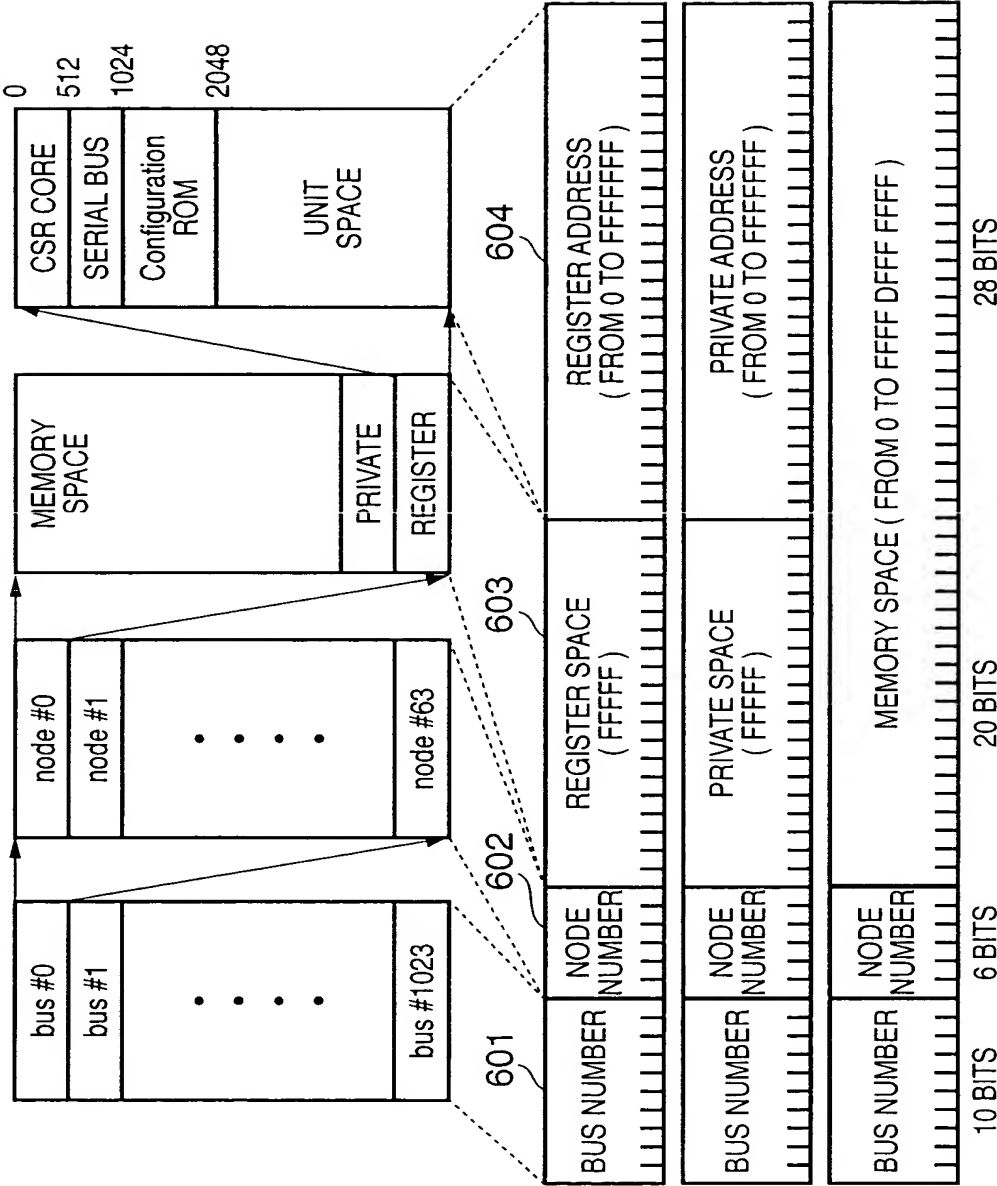
**FIG. 5**

FIG. 6



**FIG. 7**

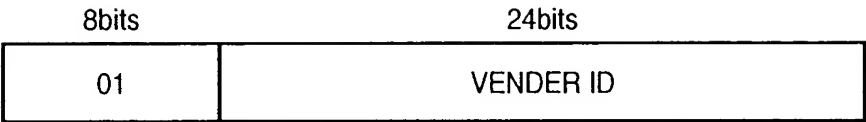
OFFSET (HEXADECIMAL)	REGISTER NAME	FUNCTION
000	STATE_CLEAR	INFORMATION OF STATUS AND CONTROL
004	STATE_SET	INFORMATION INDICATING WRITE ENABLE/DISABLE OF STATE_CLEAR
008	NODE_IDS	BUS ID + NODE ID
00C	RESET_START	RESET BUS BY WRITE IN THIS AREA
010~014	INDIRECT_ADDRESS, INDIRECT_DATA	REGISTER FOR ACCESSING ROM LARGER THAN 1K
018~01C	SPLIT_TIMEOUT	VALUE OF TIMER FOR DETECTING TIME-OUT OF SPLIT TRANSACTION
020~02C	ARGUMENT,TEST_START, TEST_STATUS	DIAGNOSIS REGISTER
030~04C	UNITS_BASE,UNITS_BOUND, MEMORY_BASE, MEMORY_BOUND	NOT IMPLEMENTED IN IEEE1394
050~054	INTERRUPT_TARGET, INTERRUPT_MASK	INTERRUPT INDICATION REGISTER
058~07C	CLOCK_VALUE, CLOCK_TICK_PERIOD, CLOCK_STOROB_E_ARRIVED, CLOCK_INFO	NOT IMPLEMENTED IN IEEE1394
080~0FC	MESSAGE_REQUEST, MESSAGE_RESPONSE	MESSAGE INDICATION REGISTER
100~17C		RESERVED
180~1FC	ERROR_LOG_BUFFER	RESERVED FOR IEEE1394

**FIG. 8**

OFFSET (HEXADECIMAL)	REGISTER NAME	FUNCTION
200	CYCLE_TIME	COUNTER FOR ISOCRONOUS TRANSFER
204	BUS_TIME	REGISTER FOR SYNCHRONIZING TIME
208	POWER_FAIL_IMMINENT	REGISTER ASSOCIATED WITH POWER SUPPLY
20C	POWER_SOURCE	
210	BUSY_TIMEOUT	CONTROL RETRY OF TRANSACTION LAYER
214 ~ 218		RESERVED
21C	BUS_MANAGER_ID	NODE ID OF BUS MANAGER
220	BANDWIDTH_AVAILABLE	MANAGE BANDWIDTH OF ISOCRONOUS TRANSFER
224 ~ 228	CHANNELS_AVAILABLE	MANAGE CHANNEL NUMBER OF ISOCRONOUS TRANSFER
22C	MAINT_CONTROL	DIAGNOSIS REGISTER
230	MAINT_UTILITY	
234 ~ 3FC		RESERVED



**FIG. 9**



CONFIGURATION ROM OF MINIMAL FORMAT

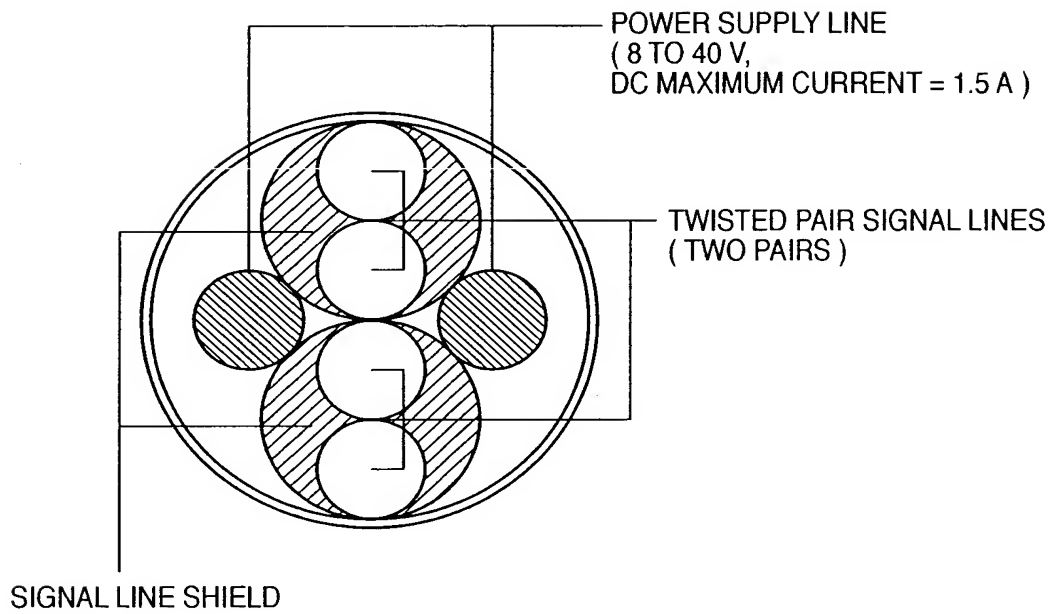
**FIG. 10**

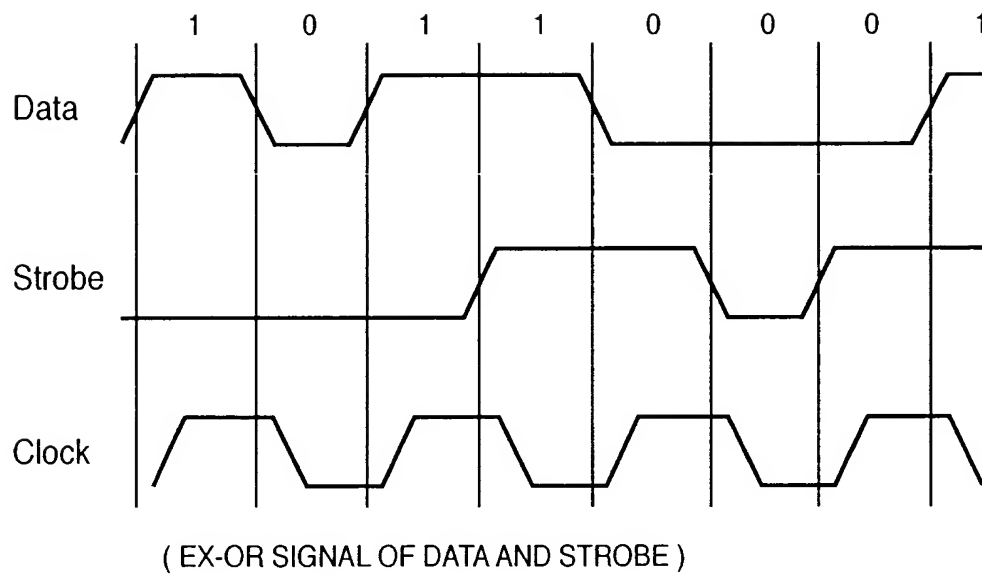
Bus Info Block Length	ROM Length	CRC	
Bus Info Block			~1001
Root Directory			~1002
Node dependent info directory			~1003
Unit directories			~1004
Root & unit leaves			~1005
Vendor dependent information			~1006

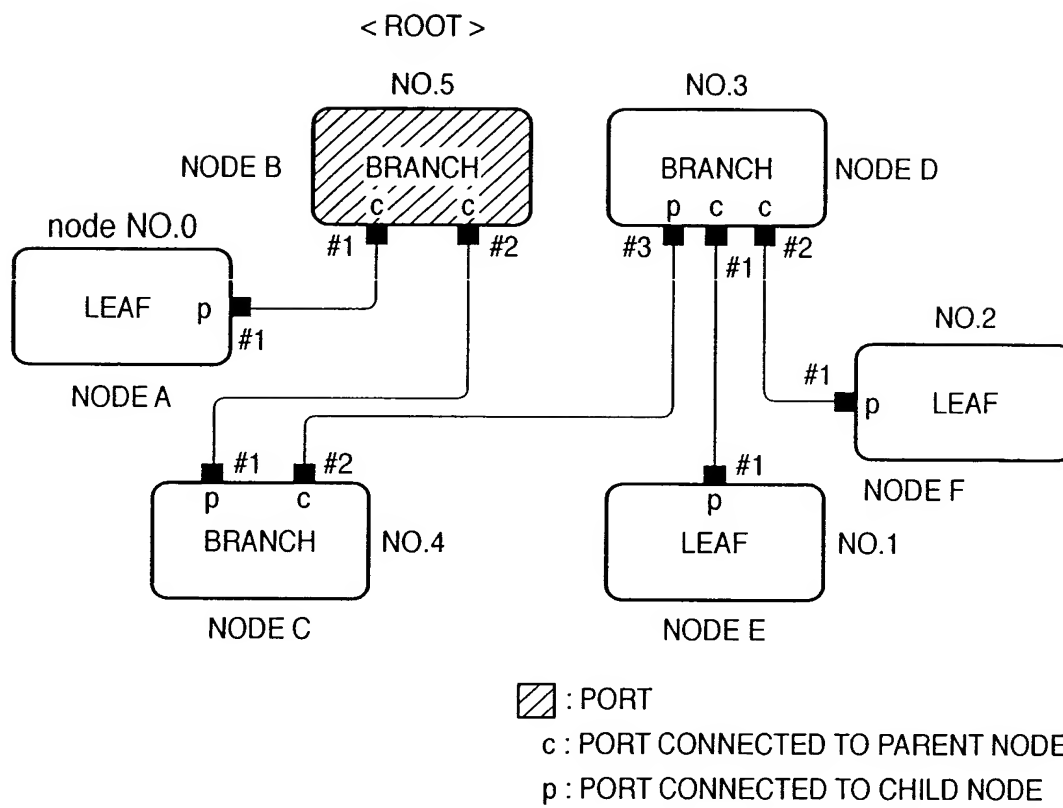
**FIG. 11**

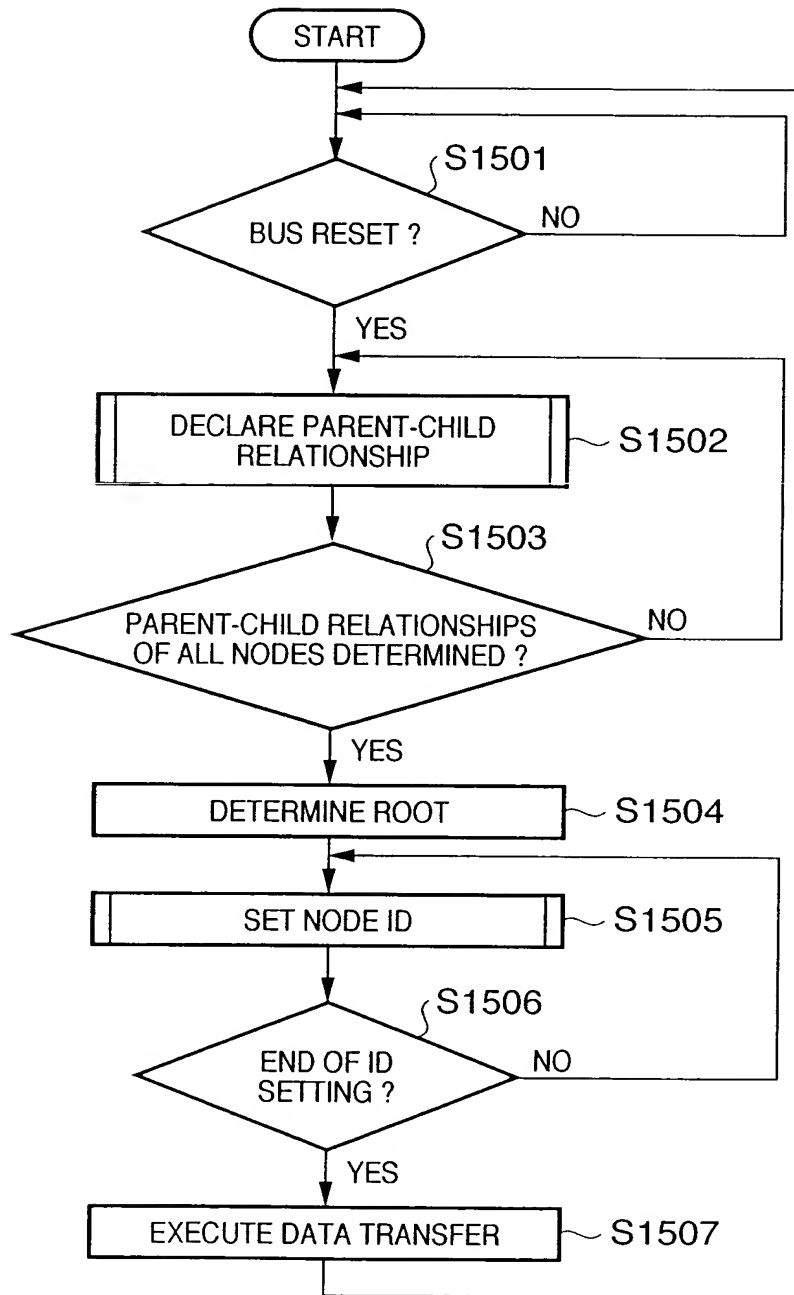
OFFSET (HEXADECIMAL)	REGISTER NAME	FUNCTION
800 ~ FFC		RESERVED
1000 ~ 13FC	TOPOLOGY_MAP	CONFIGURATION INFORMATION OF SERIAL BUS
1400 ~ 1FFC		RESERVED
2000 ~ 2FFC	SPEED_MAP	INFORMATION OF TRANSFER RATE OF SERIAL BUS
3000 ~ FFFC		RESERVED

**FIG. 12**



**FIG. 13**



**FIG. 15**

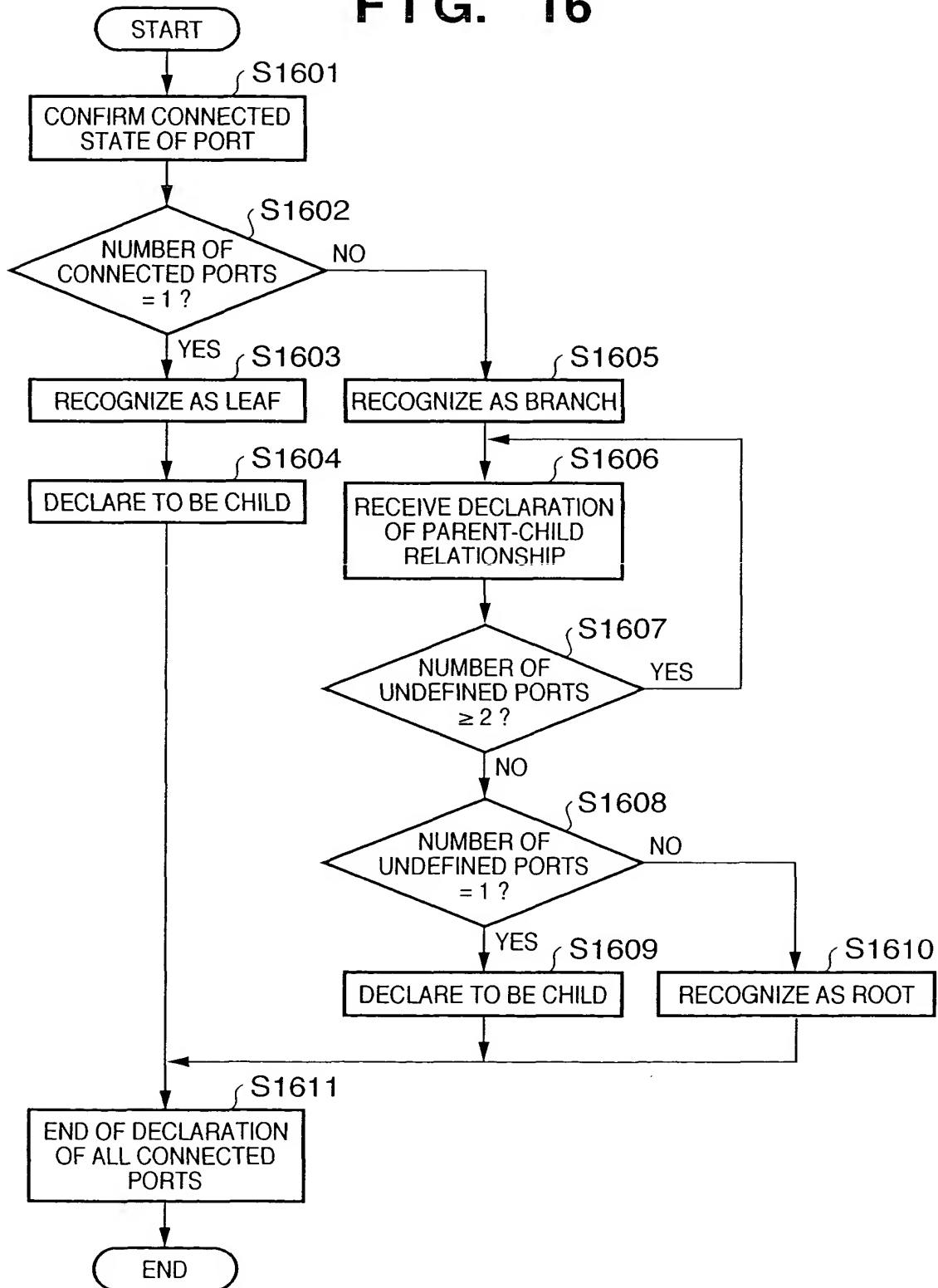
**FIG. 16**



FIG. 17

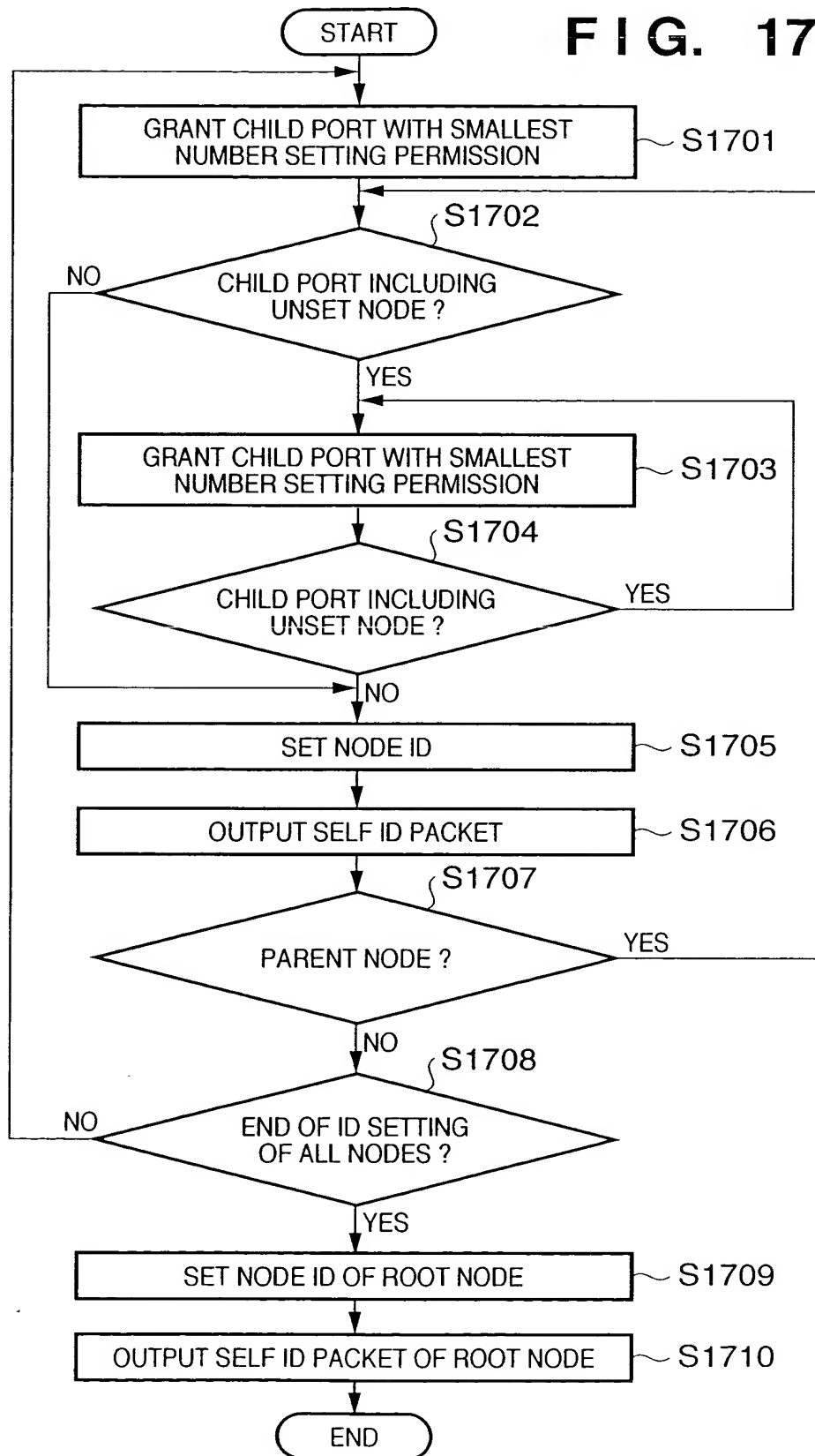
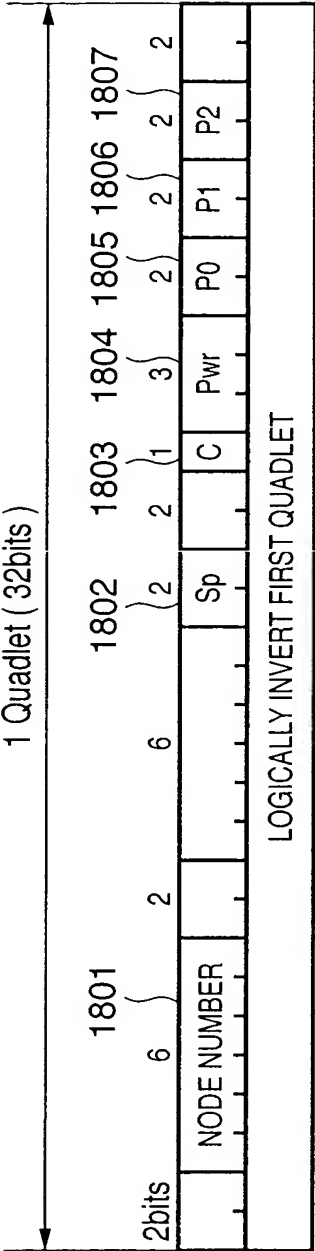
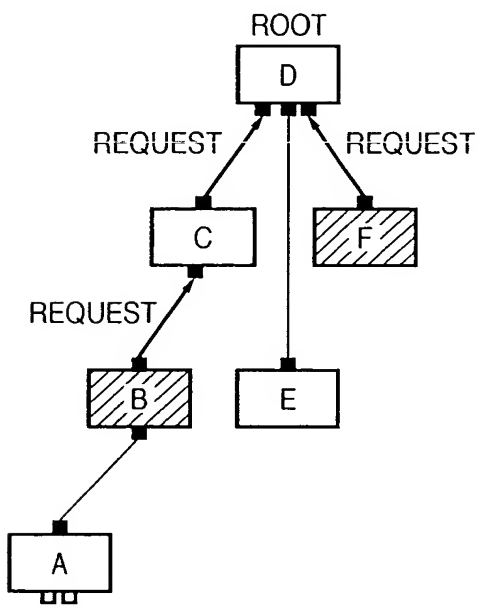


FIG. 18



**FIG. 19A**

REQUEST RIGHT TO USE BUS

**FIG. 19B**

PERMIT RIGHT TO USE BUS

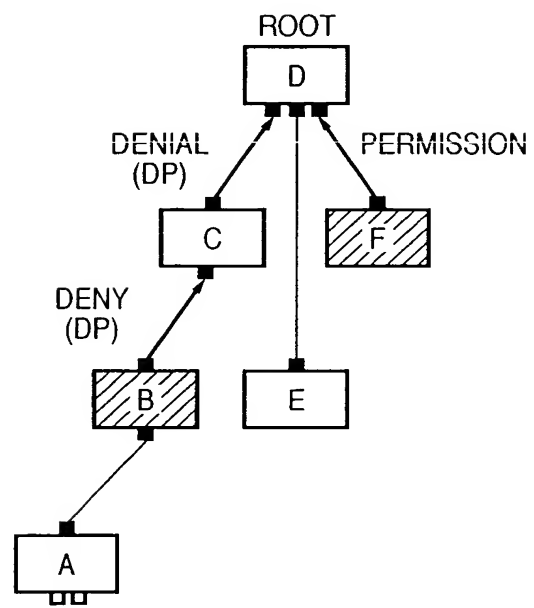


FIG. 20

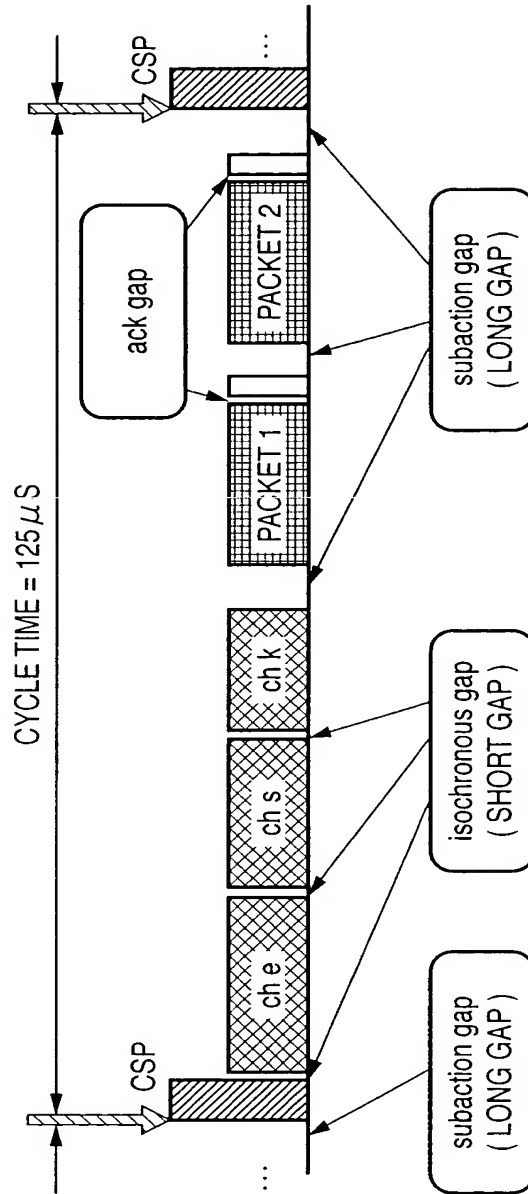


FIG. 21

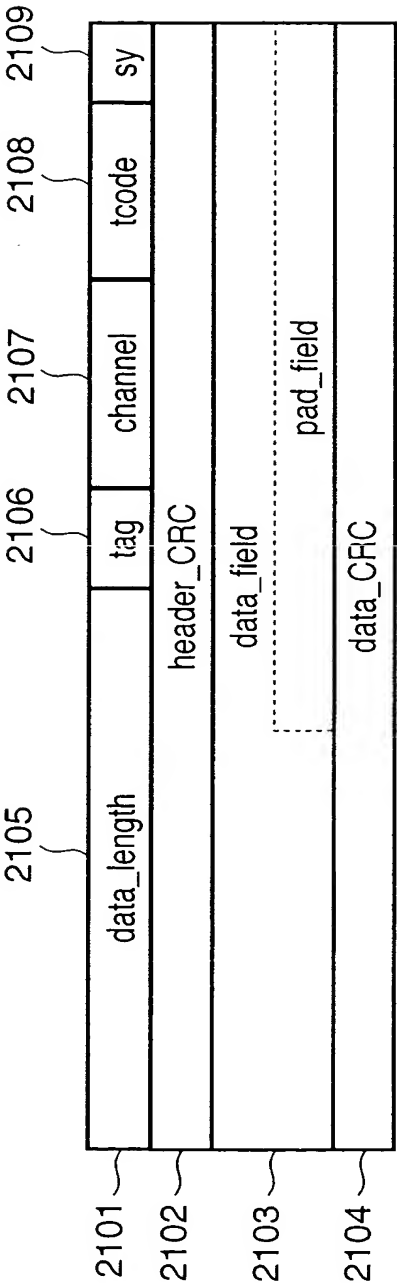
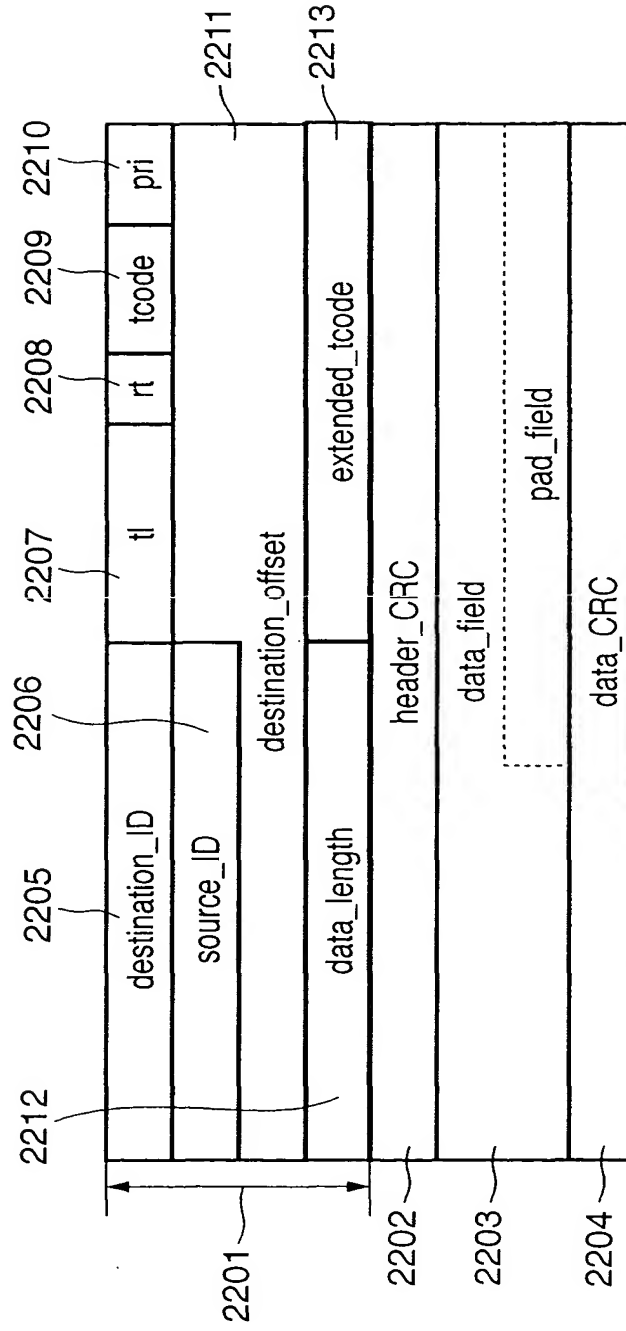


FIG. 22



**FIG. 23**

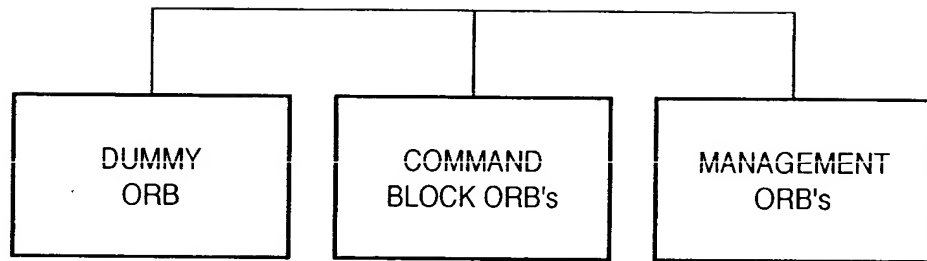


FIG. 24

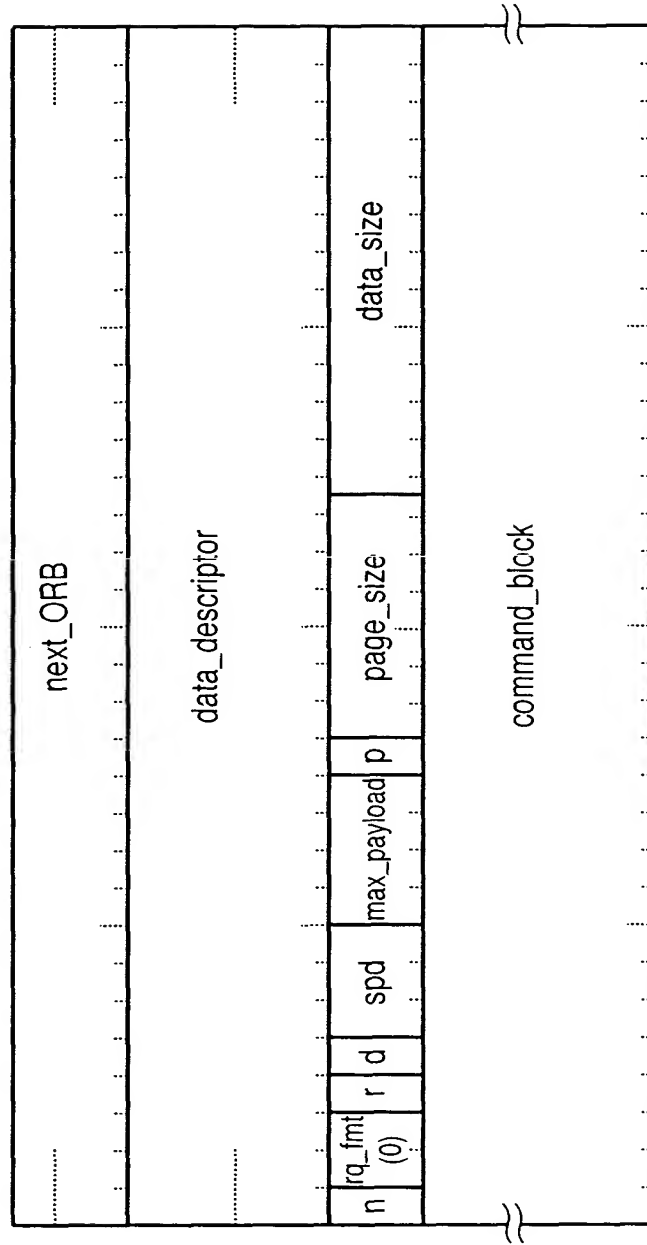
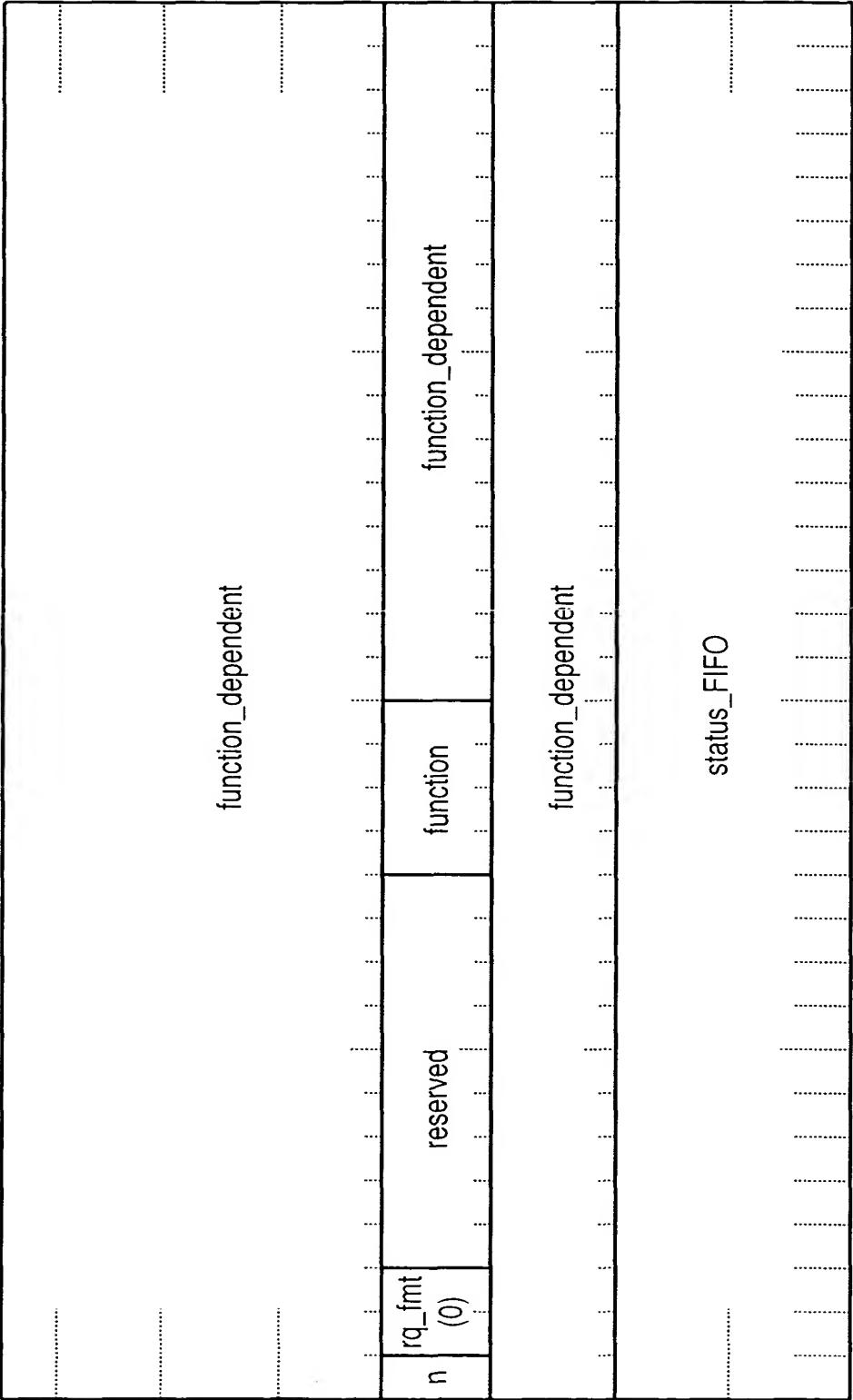




FIG. 25



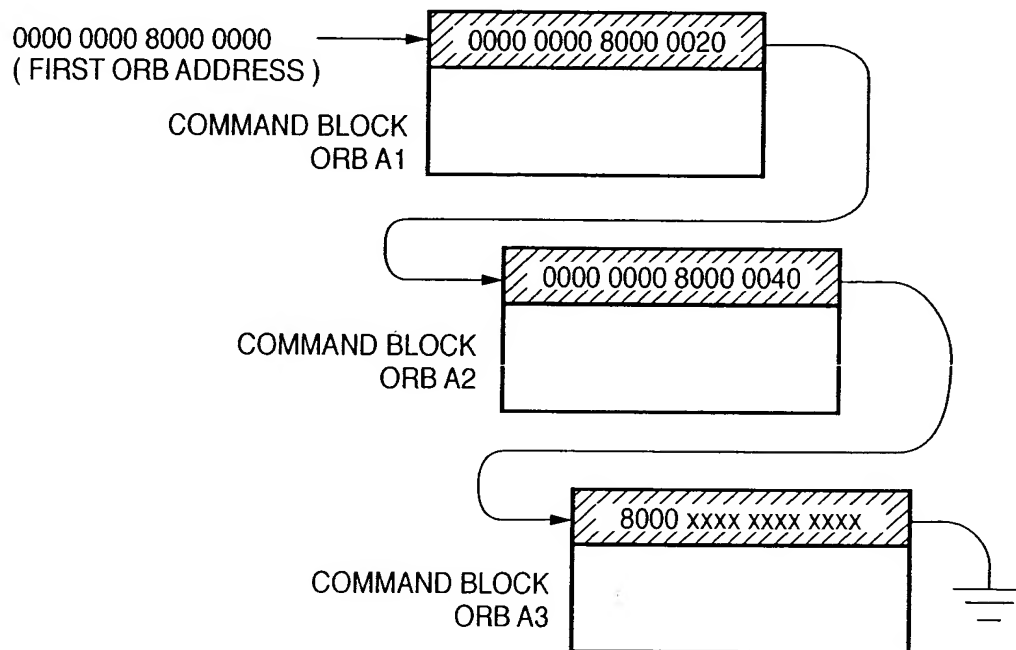
**FIG. 26**

FIG. 27

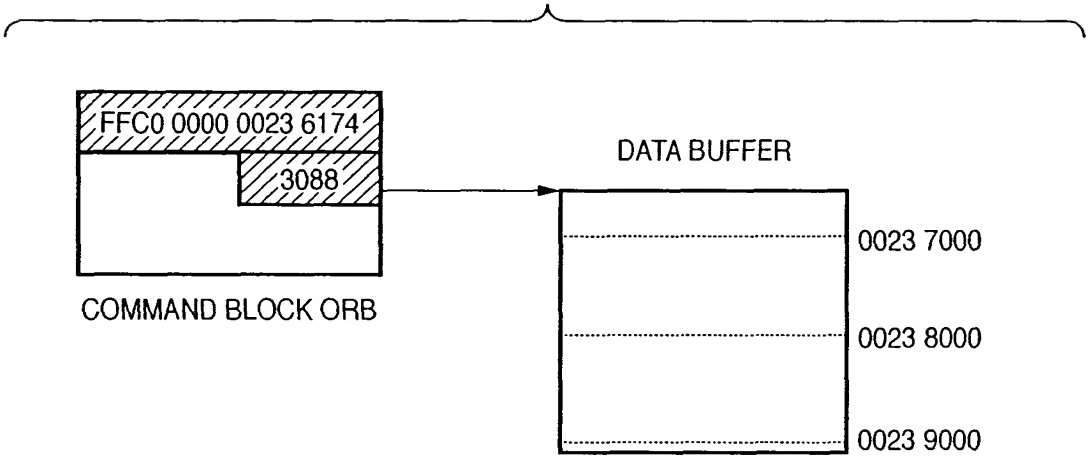


FIG. 28

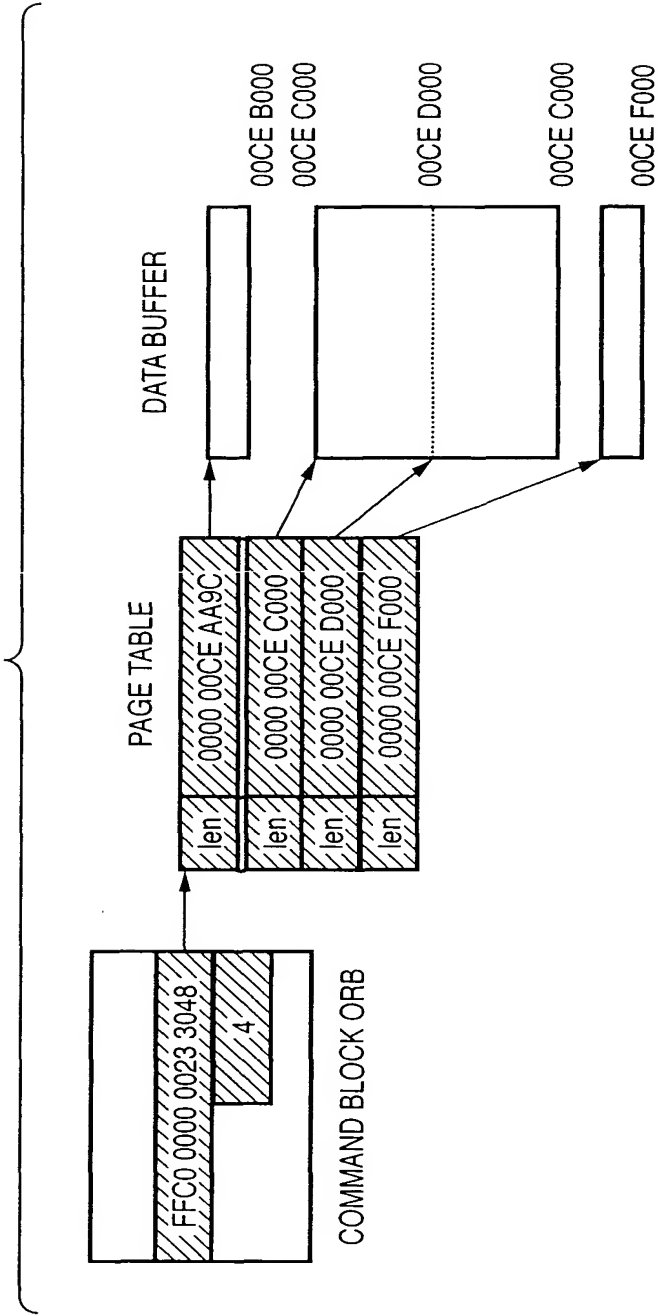


FIG. 29

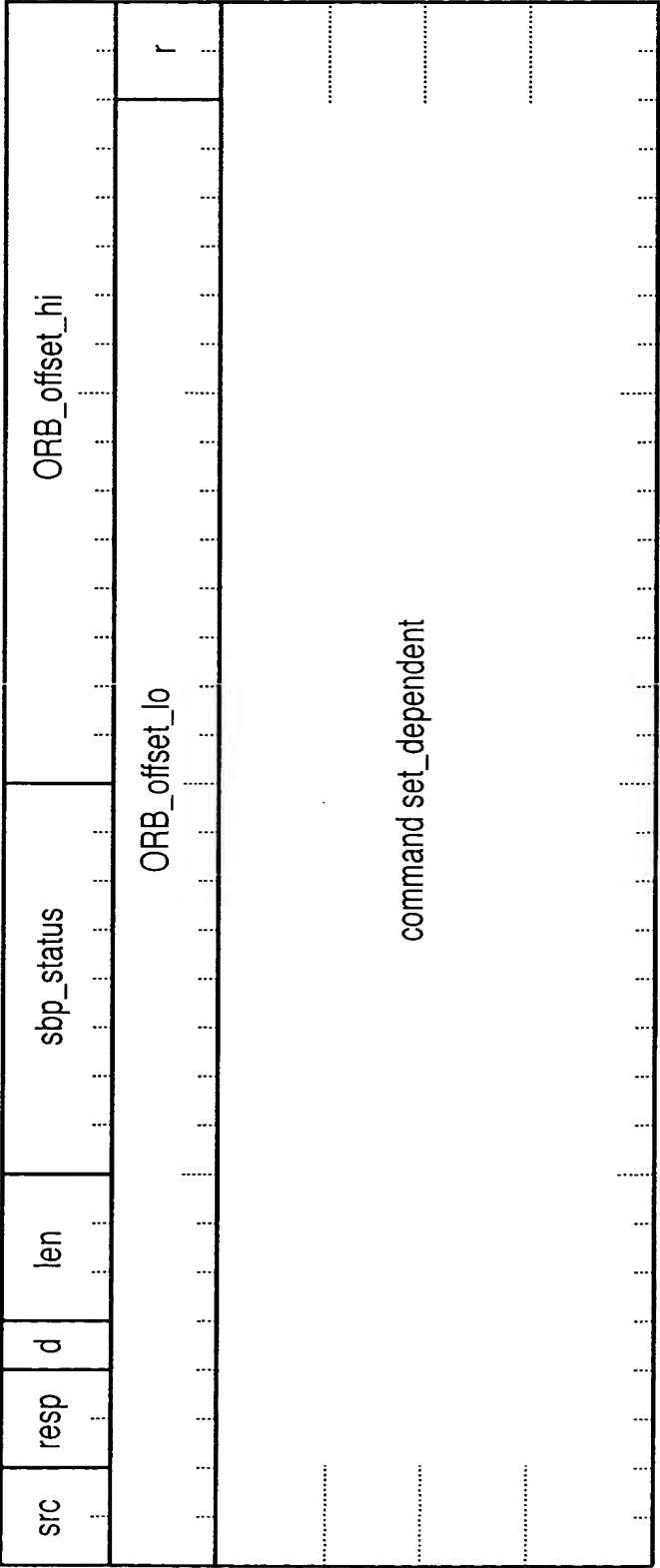


FIG. 30

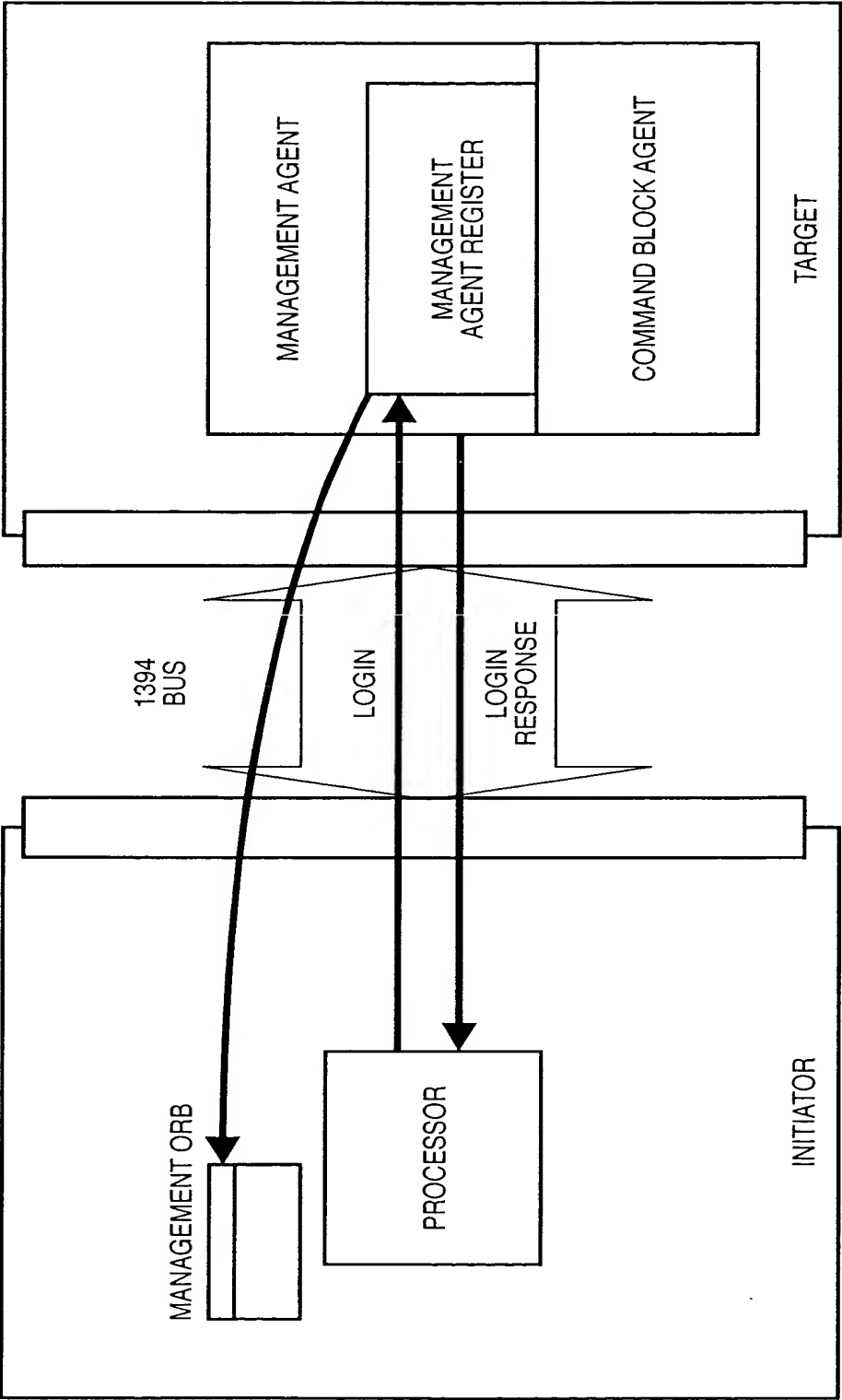


FIG. 31

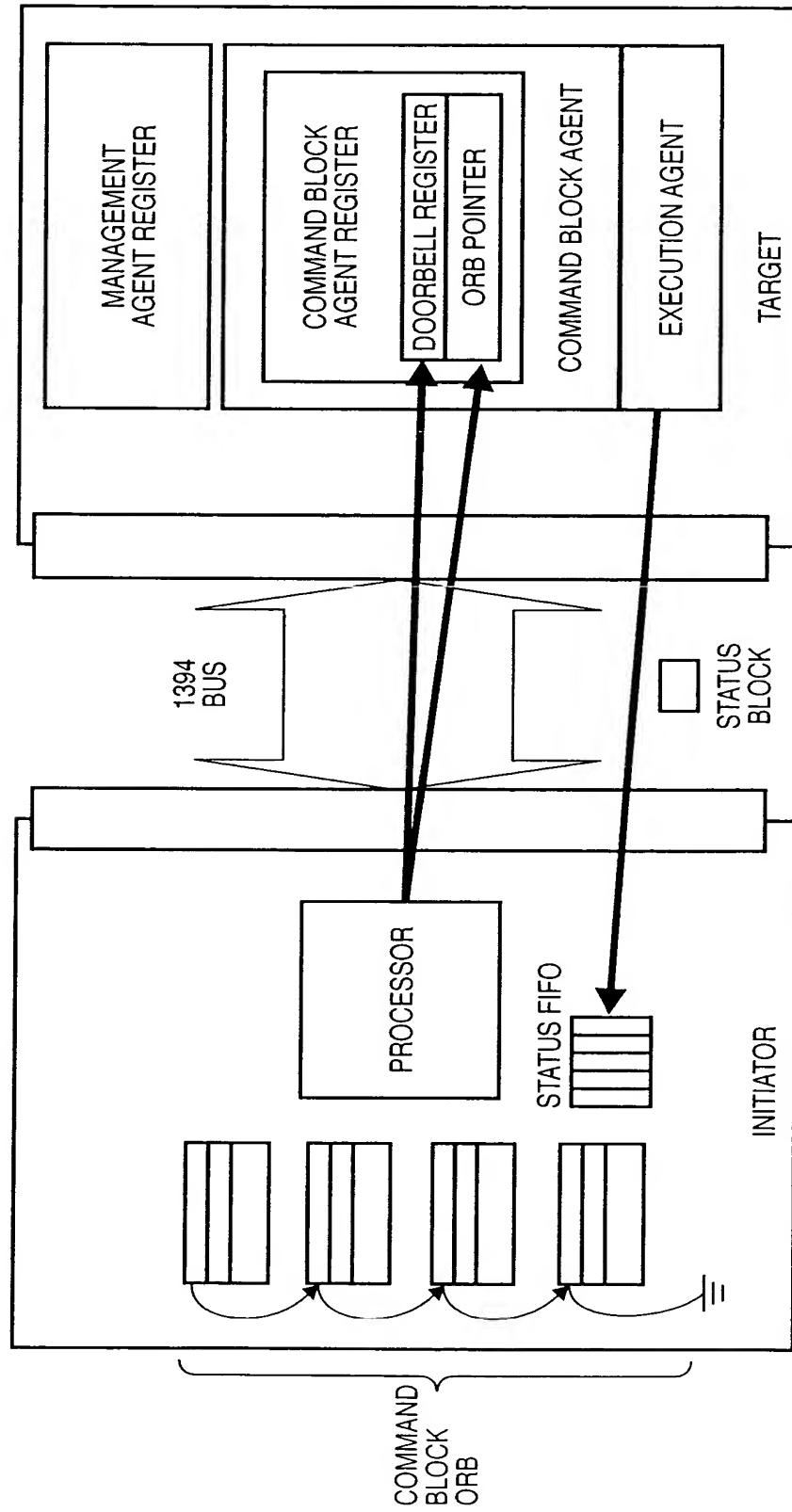


FIG. 32

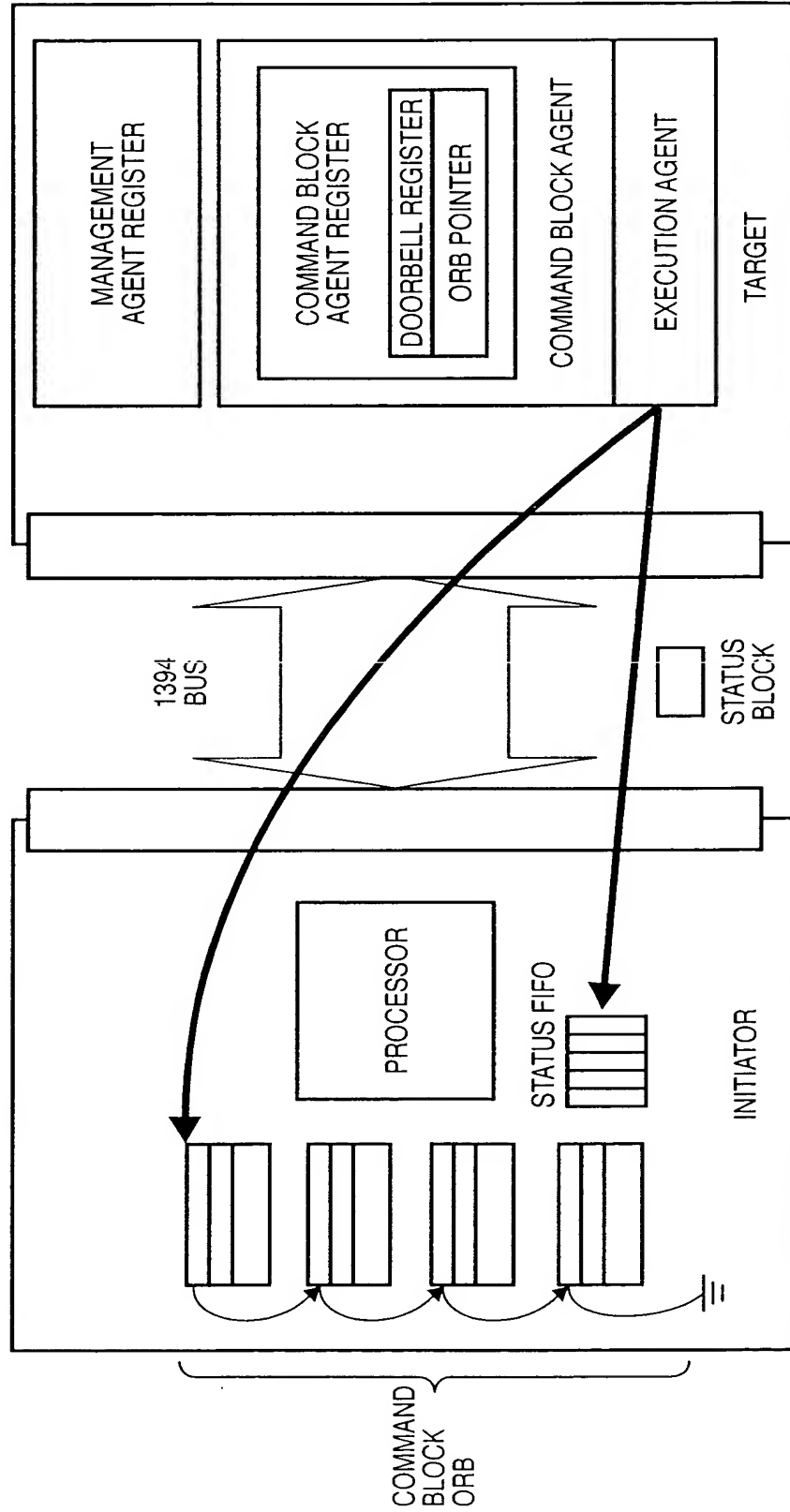




FIG. 33

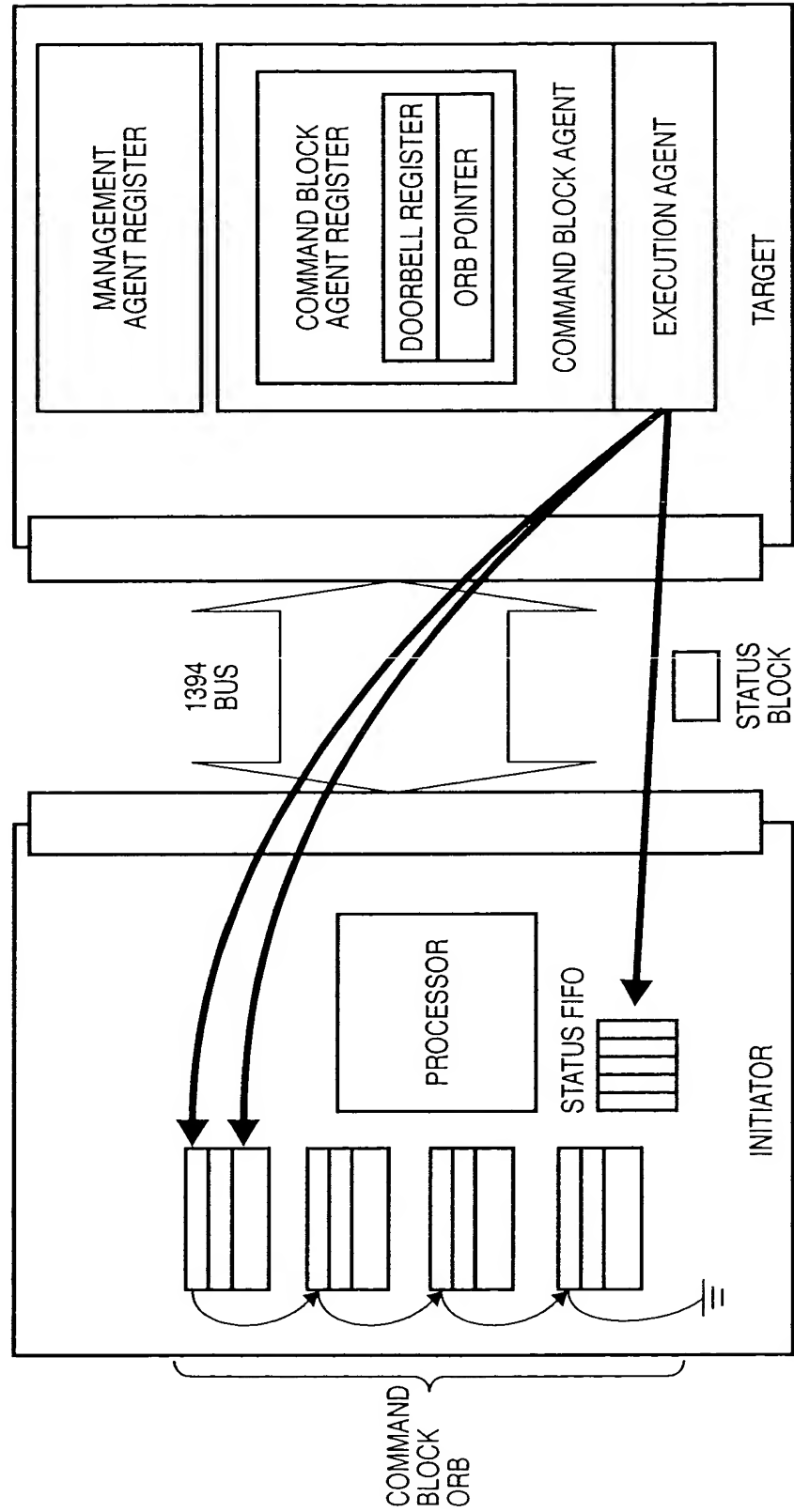
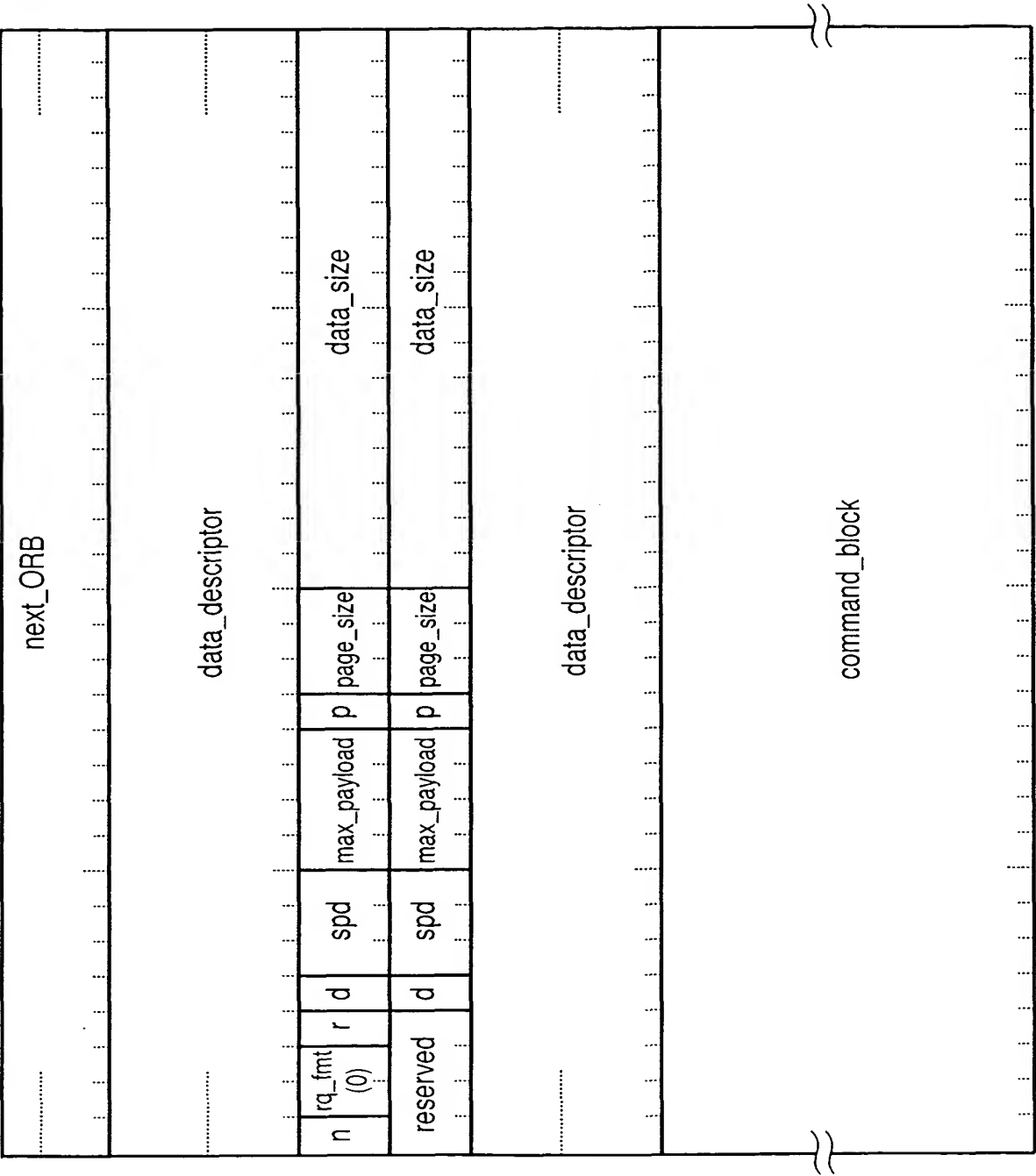


FIG. 34



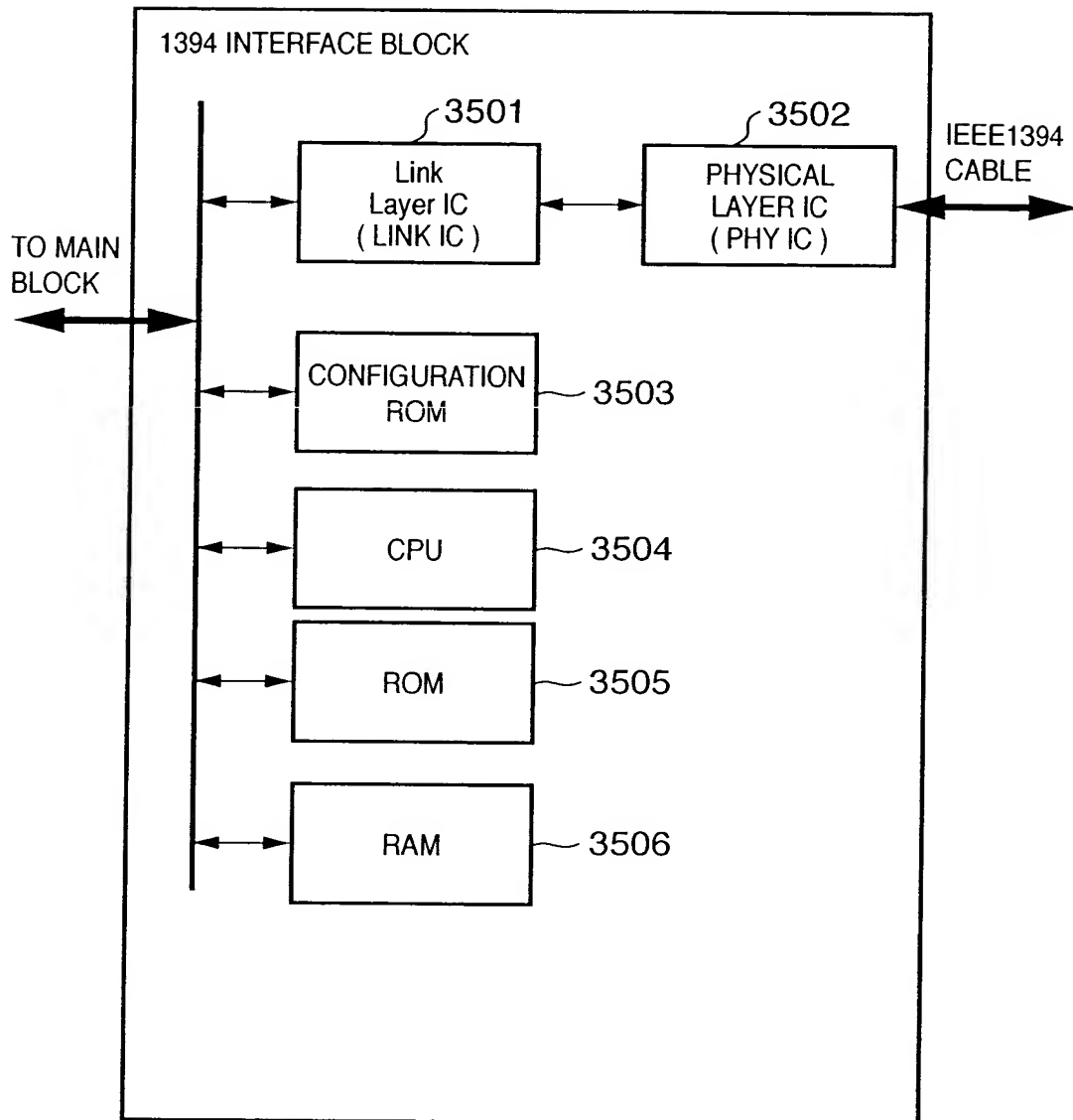
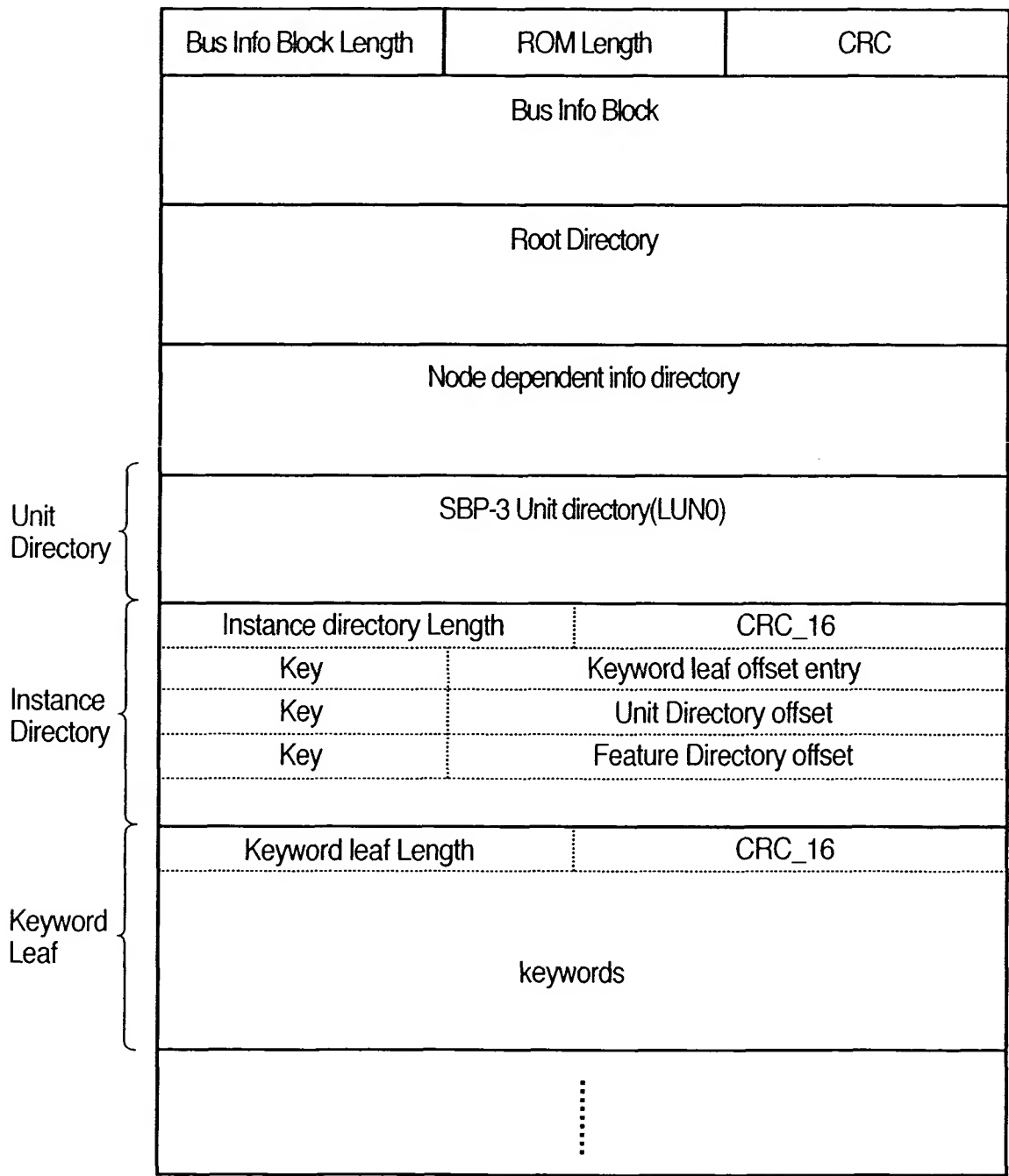
**FIG. 35**

FIG. 36



**FIG. 37**

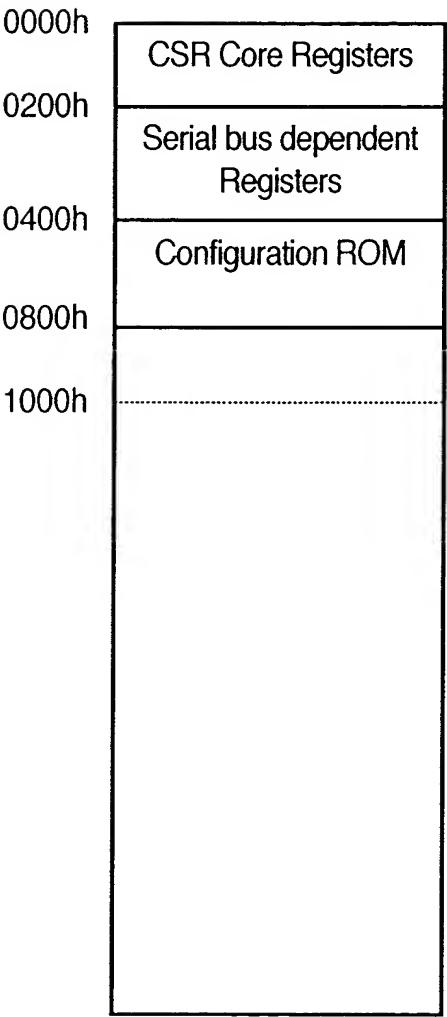


FIG. 38

OFFSET (HEXADECIMAL)	REGISTER NAME	FUNCTION
200	CYCLE_TIME	COUNTER FOR ISOCRONOUS TRANSFER
204	BUS_TIME	REGISTER FOR SYNCHRONIZING TIME
208	POWER_FAIL_IMINENT	REGISTER ASSOCIATED WITH POWER SUPPLY
20C	POWER_SOURCE	
210	BUSY_TIMEOUT	CONTROL RETRY OF TRANSACTION LAYER
214~218		RESERVED
21C	BUS_MANAGER_ID	NODE ID OF BUS MANAGER
220	BANDWIDTH_AVAILABLE	MANAGE BANDWIDTH OF ISOCRONOUS TRANSFER
224~228	CHANNELS_AVAILABLE	MANAGE CHANNEL NUMBER OF ISOCRONOUS TRANSFER
22C	MAINT_CONTROL	DIAGNOSIS REGISTER
230	MAINT_UTILITY	
234~23C		RESERVED

# FIG. 39

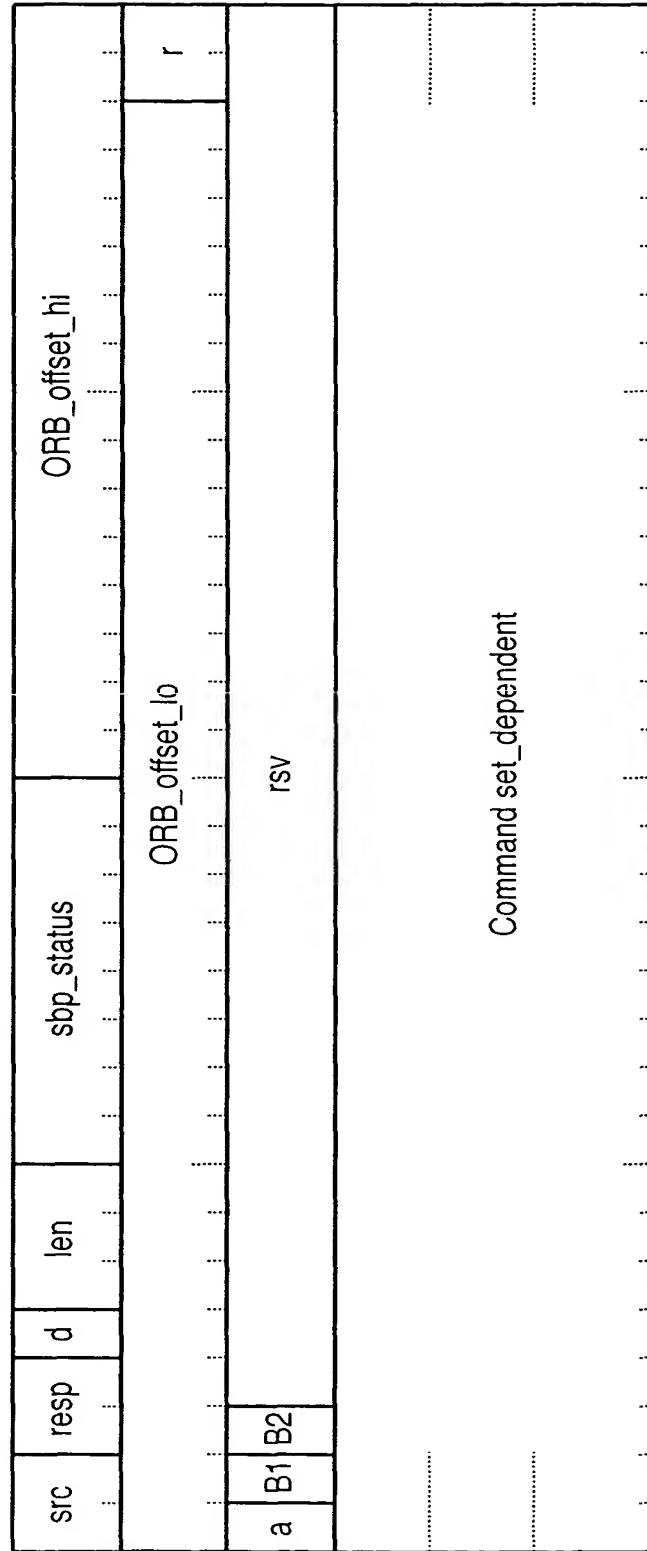


FIG. 40

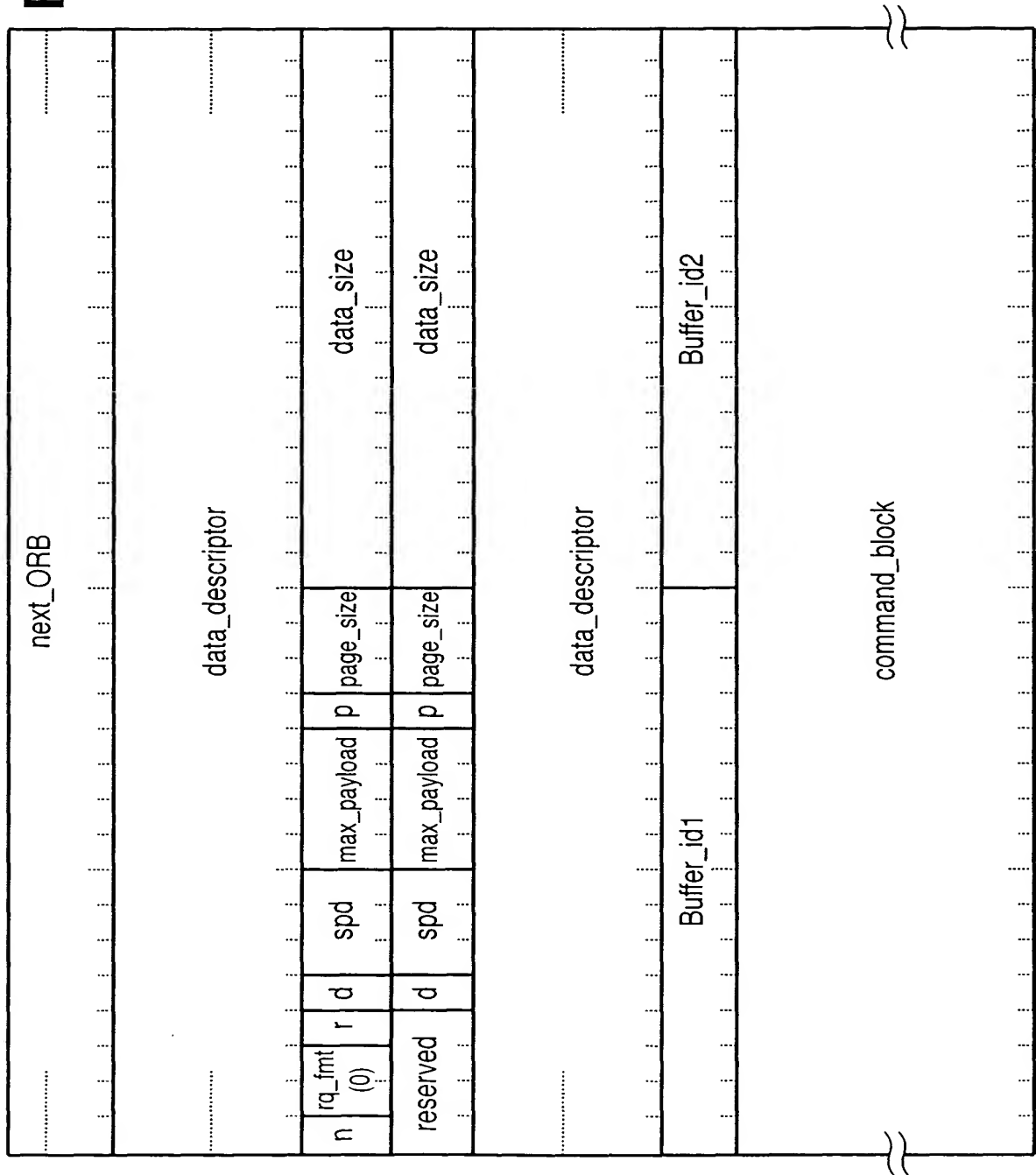




FIG. 41

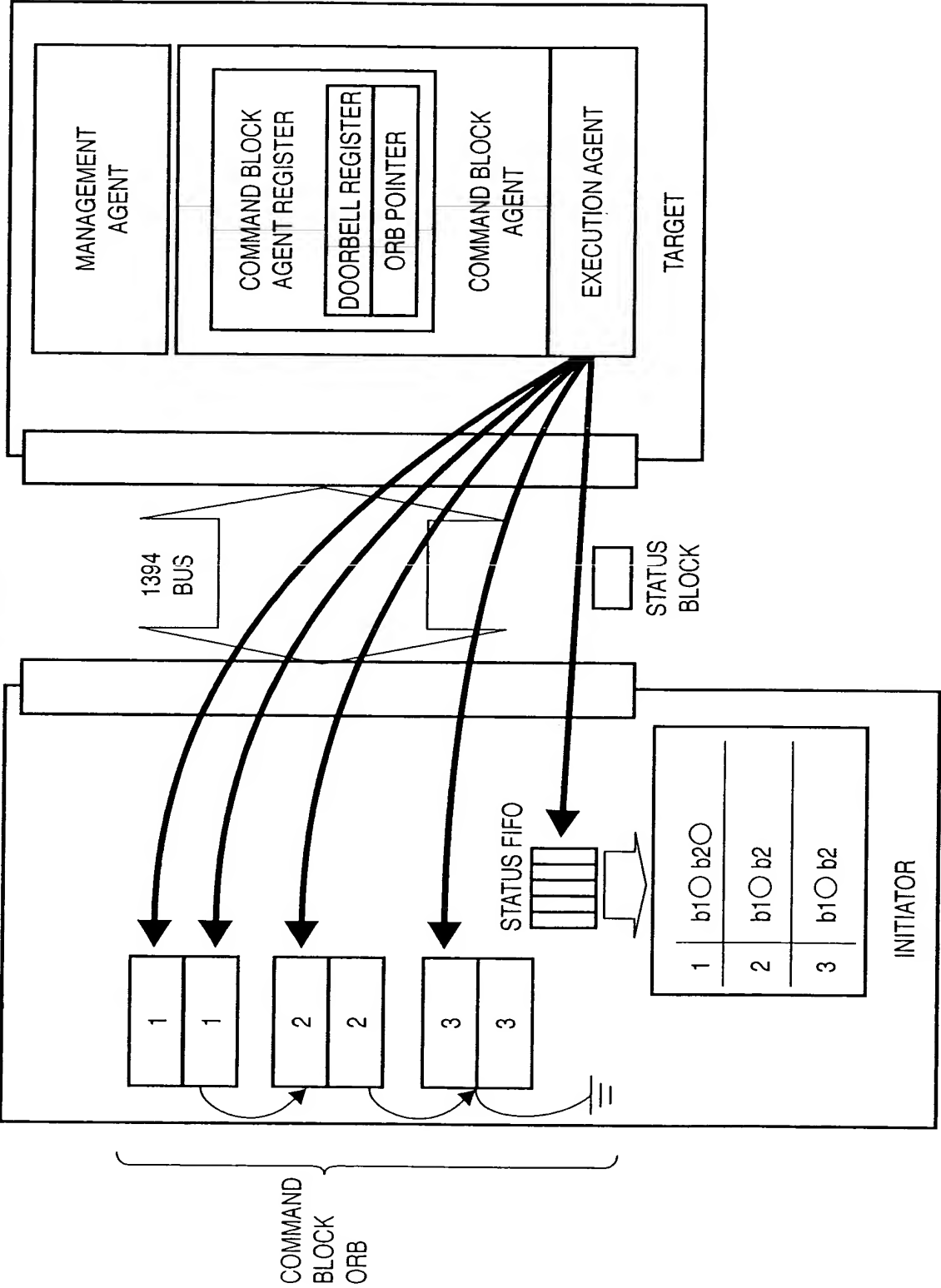


FIG. 42

